

GREGORY S. YOVANOFCURRICULUM VITAE (March, 2018) – yovanof@gmail.com

1. Surname : **YOVANOF**
2. Name : **GREGORY**
3. Date of birth : October, 19, 1958
4. Nationality : Greek // Dual citizenship: U.S.A and Greece.
5. Education : PhD in Electrical Engineering

<i>Institutions :</i>	University of Southern California (USC), USA
<i>Date: from(mo/year) to (mo/year)</i>	August 1988 // From Sep 1984 to Aug 1988
<i>Degree(s) or diploma(s) :</i>	Ph.D. in Electrical Engineering – Communications
<i>Institutions :</i>	University of Southern California (USC), USA
<i>Date: from(mo/year) to (mo/year)</i>	Aug 1984 // From Sep 1984 to Aug 1988
<i>Degree(s) or diploma(s) :</i>	M.Sc. in Signal Processing, Electrical Engineering
<i>Institutions :</i>	National Technical University of Athens, Greece
<i>Date: from(mo/year) to (mo/year)</i>	August 1982 // From Sep 1977 to Aug 1982
<i>Degree(s) or diploma(s) :</i>	Diploma in Electrical Engineering

6. Language skills: (Mark 1 to 5 for competence, where 5 is the highest)

<i>Language</i>	<i>Passive</i>	<i>Spoken</i>	<i>Written</i>
Greek	5	5	5
English	5	5	5

7. Membership of professional bodies: *Senior Member* of IEEE (Institute of Electrical & Electronics Engineers). Member of the Technical Chamber of Greece.

8. Other skills (e.g. computer literacy, etc.): High-Tech Business Development; Start-up Funding; Strategy of Research, Technology & IP (Intellectual Property). Start-up mentor & coach (MIT Enterprise Forum Greece, BlueGrowth Piraeus).

9. Present position: Professor of *Innovation Management & Entrepreneurship* at *Athens Information Technology (AIT)* and a former Associate Dean of the AIT.

10. Years within the company: Sixteen (Nov 2002 – Present)

11. **Key qualifications:** Over thirty-year experience in Research & Business Development in the ICT industry as well as in the Management of Academic Education & Research Programs. Areas of expertise: Applications of information theory; Broadband wireless communications; Multimedia systems; Strategic management of Innovation and Entrepreneurship; Commercialization of High-tech Products & Service; Strategic Management of Clusters of Innovation. Technical management in the area of multimedia chip design and production, involved in all stages of a product life cycle – product definition, algorithm development and hardware design, foundry relationships, wafer testing, system integration and customer support. Business Development experience with hi-tech companies from the greater Pacific-Rim region. Exposed to the structured environment of a big corporation as well as the creative chaos of a start-up. Member of an executive team that succeeded in raising VC funds to launch a company in Silicon Valley. An active member of the Greek entrepreneurial [ecosystem](#). Managing Director, STRATEGIS – Maritime ICT Cluster in Piraeus, Greece http://www.ait.gr/faculty/G_Yovanof.asp

12. Specific experience (in non-EU member countries)

<i>Country</i>	<i>Date : from (mo/year) to (month/year)</i>	<i>Name and brief description of the project</i>
USA	Mar. 2000 – Jun. 2001	Bitmath Inc. HELIOS Project: Optical Networking Forward Error Correction (FEC). Managed the development of an Intellectual Property (IP) core implementing a 40 Gbit/s FEC supporting OC-192, OC-768 data rates.
USA	Feb 1997 – Jan. 2000	Cyclonics, Inc. DVD-Data controller chip: Manager - ASIC product development of multimedia chip (ECC, demodulation, DVD-navigation, embedded controller and firmware). This chip reached mass-market production volume and gained several market awards for its performance; it was used by Nakamichi (Japan) and AKAI in the award-winning DV-P4000 DVD-player.
USA	Feb 1993 – Jan. 1997	HP Labs/Computer Peripherals Lab, Palo Alto, CA. Compression & Digital Video project: Team member responsible for data compression and image processing for <i>computer peripheral devices</i> . Key contributor to the FlashPix image format. Co-developed the first JPEG ASIC chip developed by HP. Main architect of a JBIG HW core developed by the ICBD division of HP. Co-designed the imaging system of the first HP digital camera.
USA	Jan. 1990 – Dec. 1990	Eastman Kodak Project: Project leader, research and development of lossless compression schemes based on arithmetic coding techniques and development of new approaches for estimating the joint entropy of color images.

13. Professional experience:

<i>Date: from (mo./yr) to (m/yr)</i>	16 years (Nov. 2002 – Present)
<i>Location</i>	Athens, Greece
<i>Company</i>	Athens Information Technology (AIT) www.ait.gr
<i>Position</i>	Professor of Innovation Management & Entrepreneurship
<ul style="list-style-type: none"> ■ Head of the “<i>Research in Innovation & Entrepreneurship</i>” – RIE research group and Graduate Program Director of the “<i>Management of Business, Innovation and Technology</i>” – MBIT, a MSc level program initially designed by Dr. Yovanof and offered for the first time in 2005. Associate Dean (Dec. 2009 – Jun. 2012). As the Head of the RIE group, Dr. Yovanof has managed EC co-funded R&D projects in Games & Gamification, Strategic Management of Innovation, Commercialization of New Technologies & Entrepreneurship. Head of the “Broadband Wireless and Sensor Networks” Research Group (Oct. 2002 – Dec. 2012): Managed funded R&D activities in broadband access networks and wireless sensor networks. ■ (May ’08 – May 2011) Adjunct Faculty, Information Networking Institute, Carnegie Mellon University. A Lecturer of the joint Harvard University, JFK School of Government and AIT executive seminar on “<i>Managing and Shaping Change in the Information Age</i>”. While at AIT, Dr. Yovanof has managed several EC co-funded research projects; has taught numerous graduate courses in ICT (Information and Computing Technologies) and in the Management of Business, Innovation and Technology; has conducted well-received executive seminars and training courses offered to the Regional ICT industry; and, has organized well received scientific and industrial workshops in emerging ICT technologies, Innovation Management, Sustainability and Regional Development. ■ (Jan. 2016 – up to date) Managing Director, STRATEGIS – Maritime Center of Excellence, & GM STRATEGIS – Maritime ICT Cluster in Piraeus, Greece, <www.strategis-cluster.com>. 	

<i>Date: from (mo./yr) to (m/yr)</i>	19 months (Mar. 2000 – Aug. 2001)
<i>Location</i>	Fremont, CA, USA
<i>Company</i>	Bitmath Inc.
<i>Position</i>	Vice President, New Business & Research
<i>Description</i>	Responsible for growing the technology portfolio and expanding the company's scope to enter the broadband and optical communications markets. Negotiated and closed four multiyear, multimillion dollar deals involving licensing IP core technology and co-developing and marketing ASIC chips - three in the area of the DVD market, and one in the high-growth area of optical networks. Head of a team that developed PHY level Forward Error Correction (FEC) technology for the optical network market.

<i>Date: from (mo./yr) to (m/yr)</i>	3 years (Jan. 1997 – Feb. 2000)
<i>Location</i>	Fremont, CA, USA
<i>Company</i>	Cyclonics Inc.
<i>Position</i>	Vice President of Business Development
<i>Description</i>	Joined Cyclonics when it started (Jan '97) as the <i>Director of Multimedia</i> to lead a multinational team developing ICs for the DVD market. Managed the design & development of a DVD-data controller chip which reached mass-market production volume and gained several awards for its performance; it was used by AKAI in the DV-P4000 DVD-player. Promoted to <i>VP of Business Development</i> in Feb. '99 and planned Cyclonics' expansion into Broadband Communications (DTV, cable modem, 3G wireless).

<i>Date: from (mo./yr) to (m/yr)</i>	5 years (Feb. 1992 – Dec. 1996)
<i>Location</i>	Palo Alto, CA, USA
<i>Company</i>	Hewlett-Packard Laboratories
<i>Position</i>	Staff Scientist
<i>Description</i>	R&D in multimedia processing as applied to computer peripherals (printers, scanners, MFPs, digital cameras). Expert in hardcopy imaging, image and video compression. Contributions to the ISO-JPEG standards committee. Key contributor to the FlashPix imaging format (a joint HP, Kodak, Microsoft, LivePicture project). Co-designed the imaging system of the first HP digital camera. Worked in the areas of MPEG video coding, non-linear image filtering, wavelet compression, JBIG/JPEG hardware.

<i>Date: from (mo./yr) to (m/yr)</i>	3.5 years (Aug. 1988 – Jan. 1992)
<i>Location</i>	Berkeley, CA, USA
<i>Company</i>	Eastman Kodak Co. - Kodak Berkeley Research/KBR,
<i>Position</i>	Member of Technical Staff
<i>Description</i>	<i>Member of Technical Staff responsible for the Analysis, design and development of high performance communication systems. Responsible for the expansion of Cyclotomics (A Kodak Company which in Sep. '89 became KBR) into the data compression area. Contributed to the design of a fast LZ, lossless compression chip and the design and prototyping of a novel high-speed optical archival system. Managed a \$350K basic research project in the area of color image compression.</i>

14. **Other:** Dr. Yovanof has served as a member of the BoD of Hellas Online S.A. An active member of the Greek entrepreneurial ecosystem, he offers strategy consultancy services to start-up companies, SMEs and clusters of innovations in the ICT, Biotech, Maritime/Blue- & Circular Economy sectors (e.g., **Managing Director**, STRATEGIS-Maritime ICT Cluster, www.strategis-cluster.com SYNERGEIA www.wtert.gr, and **BIONIAN**, the first Life Sciences Cluster in Greece and SEE). He participates in a Focus Group for the development of the **Blue Energy Hub** & Blue-Energy National Strategy in Greece. Dr. Yovanof has been an adjunct professor at the Carnegie Mellon University (CMU). **LinkedIn:** <http://www.linkedin.com/in/gregoryyovanof>